



ADVANCED MATERIALS AND DEVICES LABORATORIES
SCHOOL OF ENGINEERING, UNIVERSITY OF TOKYO

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SEMINAR ANNOUNCEMENT

Dr. Ting-Ping Liu

Director, High-Speed Circuits Research Department
Agere Systems, Holmdel, NJ, USA

“INTEGRATED CIRCUITS FOR OPTICAL COMMUNICATIONS”

DATE: Wednesday, June 20, 2001

TIME: 10:30 am-11:30 am

PLACE: Rm. 400

4th Floor, Engineering Building 10

ABSTRACT

Recent explosion of bandwidth needs for the telecommunication network has re-kindled both research and development activities in high-data-rate electronics. This talk will describe the broadband physical layer integrated circuits research program for optical communications in the Agere HSC research department. Couples of high-speed ICs targeted for SONET and 10G Ethernet applications were implemented in various silicon IC technologies, including CMOS as well as Si and SiGe BiCMOS technologies, and the test results were quite promising.

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Refreshments will be provided.

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